**LESSON PLAN**

**Subject Code & Name: Microprocessor & Micro controller**

**Branch: E.C.E-C Class / Semester: III/IV-SEM 1I Academic Year:2016-17**

**Faculty: D.V.L.N.Sastry**

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| **Period** | **Date (Tentative)** | **Topic** | **Unit No.** | **Teaching Methodology** | **Remarks** | **Corrective action upon review** |
|  |  | **Micro Processor 8086** | **I** |  |  |  |
| 1 | 05.12.16 | Introduction |  | CR |  |  |
| 2 | 06.12.16 | Architecture |  | PPT |  |  |
| 3 | 07.12.16 | Architecture |  | PPT |  |  |
| 4 | 08.12.16 | Register organization |  | PPT |  |  |
| 5 | 13.12.16 | Memory segmentation & organization |  | PPT |  |  |
| 6 | 14.12.16 | Memory segmentation & organization |  | CR |  |  |
| 7 | 15.12.16 | Signal description |  | PPT |  |  |
| 8 | 19.12.16 | PIN diagram |  | PPT |  |  |
| 9 | 20.12.16 | Addressing Modes |  | **CR** |  |  |
| 10 | 21.12.16 | Addressing Modes |  | CR |  |  |
| 11 | 22.12.16 | Assembler Directives |  | CR |  |  |
| 12 | 26.12.16 | Assembler Directives |  | CR |  |  |
| 13 | 27.12.16 | Procedures |  | CR |  |  |
| 14 | 28.12.16 | Macros |  | CR |  |  |
| 15 | 29.12.16 | Timing Diagrams |  | CR |  |  |
| 16 | 02.01.16 | Address calculation |  | CR |  |  |
|  |  | **Assembly language programming of 8086** | **II** |  |  |  |
| 17 | 03.01.12 | Introduction |  | CR |  |  |
| 18 | 05.01.12 | Instruction set |  | CR |  |  |
| 19 | 09.01.12 | Assembly language programmes |  | CR |  |  |
| 20 | 10.01.12 | Assembly language programmes |  | CR |  |  |
| 21 | 11.01.12 | Introduction to stack |  | CR |  |  |
| 22 | 17.01.12 | Stack operation |  | CR |  |  |
| 23 | 18.01.12 | Stack structure |  | CR |  |  |
| 24 | 23.01.12 | Classification of interrupts |  | CR |  |  |
| 25 | 24.01.12 | ISR |  | CR |  |  |
| 26 | 25.01.12 | IVT |  | CR |  |  |
|  |  | **Advanced Micro Processors** | **III** |  |  |  |
| 28 | **30.01.17** | Introduciton |  | **CR** |  |  |
| 29 | 31.01.17 | Architecture of 80386 |  | **PPT** |  |  |
| 30 | 01.02.17 | Explanation on Architecture |  | PPT |  |  |
| 31 | 02.02.17 | Features |  | CR |  |  |
| 32 | 06.02.17 | Register organization |  | PPT |  |  |
| 33 | 07.02.17 | Signal description |  | PPT |  |  |
| 34 | 08.02.17 | Data types |  | CR |  |  |
| 35 | 09.02.17 | Physical address calculation |  | CR |  |  |
| 36 | 13.02.17 | Modes of operation |  | CR |  |  |
| 37 | 14.02.17 | Types of modes |  | CR |  |  |
| 38 | 15.02.17 | Addressing modes |  | CR |  |  |
| 39 | 16.02.17 | Segmentation |  | PPT |  |  |
| 40 | 20.02.17 | Segmentation |  | PPT |  |  |
| 41 | 21.02.17 | Paging unit |  | PPT |  |  |
| 42 | 22.02.17 | Paging unit |  | PPT |  |  |
| 43 | 23.02.17 | Introduciton to 80486 |  | PPT |  |  |
|  |  | **Interfacing with 8086** | **IV** |  |  |  |
| 44 | 27.02.17 | Introduction |  | CR |  |  |
| 45 | 28.02.17 | Programmable interrupt controller 8259A |  | PPT |  |  |
| 46 | 06.03.17 | 8255 PPI |  | PPT |  |  |
| 47 | 07.03.17 | Modes of operation |  | PPT |  |  |
| 48 | 08.03.17 | Control words of 8255 |  | PPT |  |  |
| 49 | 09.03.17 | 8257 DMA controller |  | PPT |  |  |
| 50 | 13.03.17 | Control words of 8257 |  | PPT |  |  |
| 51 | 14.03.17 | Key board / display controller 8279 |  | PPT |  |  |
| 52 | 15.03.17 | USART 8251 |  | PPT |  |  |
| 53 | 16.03.17 | Control words of 8251 |  | PPT |  |  |
|  |  | **Micro controller 8051** |  |  |  |  |
| 54 | **20.03.17** | introduction |  | **CR** |  |  |
| 55 | 21.03.17 | Architecture |  | PPT |  |  |
| 56 | 22.03.17 | Architecture |  | PPT |  |  |
| 57 | 23.03.17 | Signal description & PIN diagram |  | PPT |  |  |
| 58 | 27.03.17 | Register set |  | CR |  |  |
| 59 | 28.03.17 | Register set |  | CR |  |  |
| 60 | 30.03.17 | Parallel I/O ports |  | CR |  |  |
| 61 | 03.04.17 | Parallel I/O ports |  | CR |  |  |
| 62 | 04.04.17 | Memory organization |  | CR |  |  |
| 63 | 06.04.17 | Interrupts |  | CR |  |  |
| 64 | 10.04.17 | Addressing modes |  | CR |  |  |
| 65 | 11.04.17 | Introduction to PIC micro controller |  | PPT |  |  |

**CR: CLASS ROOM PPT: POWER POINT PRESENTATION LCD**